

**TERMINAL DISCLAIMER TO OBVIATE A DOUBLE PATENTING
REJECTION OVER A PRIOR PATENT**

Docket Number (Optional)

1374.39932CX1

In re Application of: Junji NOGUCHI

Application No.: 10/811,927

Filed: 3/30/2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND FABRIACTION METHOD FOR SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

The owner*, Renesas Technology Corp., of 100 percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 and 173, as presently shortened by any terminal disclaimer, of prior Patent No. 6,348,410. The owner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and the prior patent are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

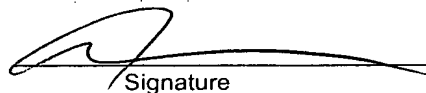
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